

S/N Unknown

#4
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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Kishore K. Chakravorty	Examiner:	Unknown
Serial No.:	Unknown	Group Art Unit:	Unknown
Filed:	Herewith	Docket:	884.267US1
Title:	ELECTRONIC ASSEMBLY COMPRISING INTERPOSER WITH EMBEDDED CAPACITORS AND METHODS OF MANUFACTURE		

1c862 U.S. PTO
09/628705
07/31/00

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
Washington, D.C. 20231

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicant respectfully requests that this Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicant further request that a copy of the 1449 form, initialed by the Examiner to indicate that all listed citations have been considered, be returned with the next official communication.

Please charge any additional fees or credit any overpayment to Account No. 19-0743.

The Examiner is invited to contact the Applicant's Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

KISHORE K. CHAKRAVORTY

By his Representatives,

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Date July 31, 2000

By Ann M. McCrackin
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CERTIFICATE UNDER 37 C.F.R. 1.10:

"Express Mail" mailing label number: EL600376723US

I hereby certify that this paper or fee is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.

Name

Shawn Hise

Signature

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	Filing Date: Herewith	Group: Unknown

U.S. PATENT DOCUMENTS

**Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
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FOREIGN PATENT DOCUMENTS

**Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation Yes No
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OTHER DOCUMENTS

**Examiner Initial	(Including Author, Title, Date, Pertinent Pages, Etc.)
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	Amey, D., et al., "Advances in MCM Ceramics", <u>Solid State Technology</u> , 143-146, (1997)
	Baniecki, J., et al., "Dielectric Relaxation of Ba _{0.7} Sr _{0.3} TiO ₃ Thin Films from 1 mHz to 20 GHz", <u>Appl. Phys. Letter</u> 72(4), 1998 American Institute of Physics, 198-500, (Jan. 1998)
	Chan, Y., et al., "Fabrication and Characterization of Multilayer Capacitors Buried in a Low Temperature Co-Fired Ceramic Substrate", <u>Active and Passive Elec. Comp. Vol. 20</u> , 215-224, (1998)
	Choi, K.L., et al., "Characterization of Embedded Passives Using Macromodels in LTCC Technology", <u>IEEE Transactions on Components, Packaging, and Manufacturing Technology, Vol. 21</u> , 258-268, (Aug. 1998)
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	Mistler, R.E., "Tape Casting: Past, Present, Potential", <u>The American Ceramic Society Bulletin</u> , 82-86, (Oct. 1998)
	Nishimura, T., et al., "3.5 V Operation Driver-Amplifier MMIC Utilizing SrTiO ₃ Capacitors for 1.95 GHz Wide-Band CDMA Cellular Phones", <u>1998 IEEE MTT-S Digest</u> , 447-450, (1998)
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Examiner	Date Considered
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*Substitute Disclosure Statement Form (PTO-1449)

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**Examiner
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	Ueda, T., et al., "GaAs MMIC Chip-sets for Mobile Communication Systems with On-chip Ferroelectric Capacitors", <u>Integrated Ferroelectrics, Vol. 7</u> , 45-60, (1995)
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	Yamasaki, K., et al., "Solder Column Interposer Grid Array - Improved CBGA Reliability", 1-9,
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